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CONFIRMATION NO. 8152

SERIAL NUMBER 10/676,695	FILING DATE 09/30/2003 RULE	CLASS 257	GROUP ART UNIT 2815	ATTORNEY DOCKET NO. MST-013-1D
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APPLICANTS

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** CONTINUING DATA *****

This application is a DIV of 10/095,901 03/11/2002 PAT 6,661,042 *One*

** FOREIGN APPLICATIONS *****

NONE One

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 12/19/2003

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged <i>W. H. Hsu</i> Examiner's Signature Initials	CA	9	12	3

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TITLE

One-transistor floating-body DRAM cell in bulk CMOS process with electrically isolated charge storage region

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